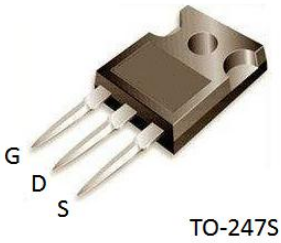
華科 HUAKE	**SMN460A**
	500V N-Channel MOSFET

● **Features：**

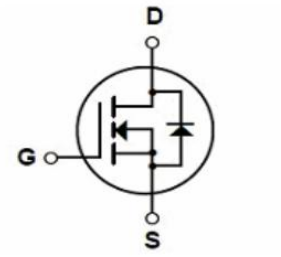
- 20.0A, 500V, $R_{DS(on)(Typ)}$ =210mΩ@V_{GS}=10V
- Low Gate Charge
- Low C_{rss}
- 100% Avalanche Tested
- Fast Switching
- Improved dv/dt Capability

● **Application:**

- High Frequency Switching Mode Power Supply
- Active Power Factor Correction



TO-247S



Absolute Maximum Ratings(Tc=25°C unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{DSS}	Drain-Source Voltage	500	V
I_D	Drain Current - Continuous(Tc=25°C)	20.0*	A
	- Continuous(Tc=100°C)	12.65*	A
I_{DM}	Drain Current -Pulsed (Note1)	80.0*	A
V_{GSS}	Gate-Source Voltage	±30	V
E_{AS}	Single Pulsed Avalanche Energy （Limit Reference Value） (Note2)	970	mJ
I_{AR}	Avalanche Current (Note1)	14.0	A
E_{AR}	Repetitive Avalanche Energy (Note1)	25.0	mJ
dv/dt	Peak Diode Recovery dv/dt (Note3)	4.5	V/ns
P_D	Power Dissipation(T_C =25°C)	232	W
	-Derate above 25°C	1.86	W/°C
Tj	Operating Junction Temperature	150	°C
Tstg	Storage Temperature Range	-55 to+150	°C

* Drain Current Limited by Maximum Junction Temperature.

Thermal Characteristics

Symbol	Parameter	Max	Unit
$R_{\theta JC}$	Thermal Resistance,Junction to Case	0.54	°C /W
$R_{\theta JA}$	Thermal Resistance,Junction to Ambient	50	°C /W

Electrical Characteristics(Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditons	Min	Typ	Max	Unit
Off Characteristics						
BV_{DSS}	Drain-source Breakdown Voltage	V_{GS}=0V ,I_D=250μA	500	--	--	V
$\triangle BV_{DSS}$ /$\triangle T_J$	Breakdown Voltage Temperature Coefficient	I_D=250μA (Referenced to 25°C)	--	0.55	--	V/°C
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=500V,V_{GS}=0V	--	--	1	μA
		V_{DS}=400V,Tc=125°C	--	--	10	μA
I_{GSSF}	Gate-Body Leakage Current,Forward	V_{GS}=+30V, V_{DS}=0V	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current,Reverse	V_{GS}=-30V, V_{DS}=0V	--	--	-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}= V_{GS}, I_D=250μA	2.0	--	4.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	V_{GS}=10 V, I_D=10.0A	--	210	270	mΩ
g_{FS}	Forward Transconductance	V_{DS}=20 V, I_D=10.0A (Note4)	--	17.5	--	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS}=25V,V_{GS}=0V, f=1.0MHz	--	3050	--	pF
C_{oss}	Output Capacitance		--	280	--	pF
C_{rss}	Reverse Transfer Capacitance		--	8.5	--	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 250 V, I_D = 20 A, R_G = 25 Ω (Note4,5)	--	35	--	ns
t_r	Turn-On Rise Time		--	57	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	86	--	ns
t_f	Turn-Off Fall Time		--	48	--	ns
Q_g	Total Gate Charge	V_{DS} = 400 V, I_D =20 A, V_{GS} = 10 V (Note4,5)	--	51	--	nC
Q_{gs}	Gate-Source Charge		--	15.8	--	nC
Q_{gd}	Gate-Drain Charge		--	20.3	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain-Source Diode Forward Current		--	--	20	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	80	A
V_{SD}	Drain-Source Diode Forward Voltage	V_{GS} =0V,I_S=20 A	--	--	1.4	V
t_{rr}	Reverse Recovery Time	V_{GS} =0V, I_S=20 A, d I_F /dt=100A/μs (Note4)	--	573	--	ns
Q_{rr}	Reverse Recovery Charge		--	7.29	--	μC

Notes:

1、Repetitive Rating:Pulse Width Limited by Maximum Junction Temperature.

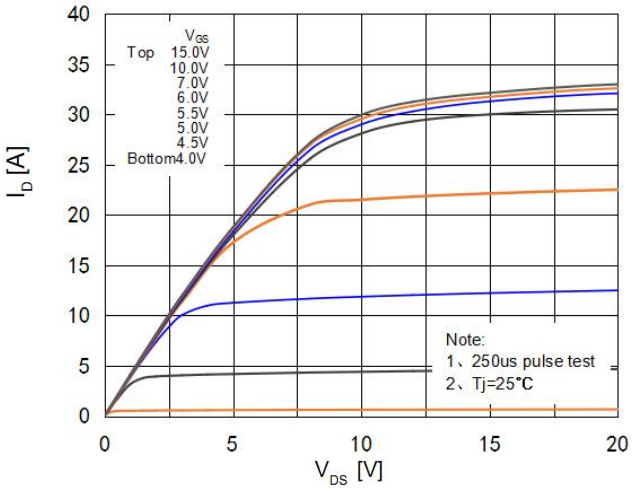
2、L = 9mH, I_{AS} =14.0A, V_{DD} = 100V, R_G = 25 Ω, Starting T_J = 25°C.

3、I_{SD}≤20.0A, di/dt≤200A/μs, V_{DD}≤BV_{DSS}, Starting T_J = 25°C.

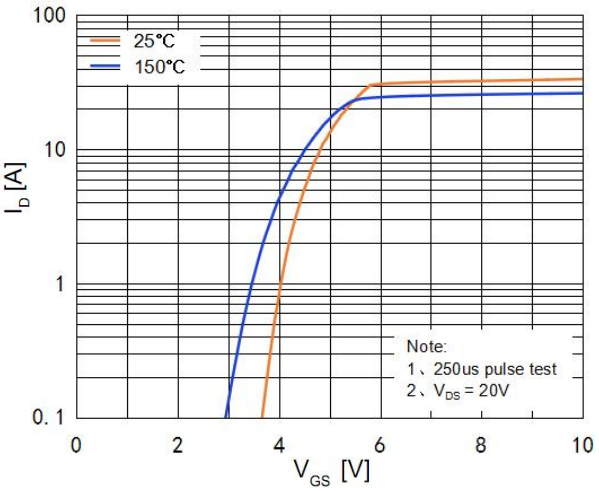
4、Pulse Test : Pulse Width ≤300 μ s, Duty Cycle≤2%.

5、Essentially Independent of Operating Temperature.

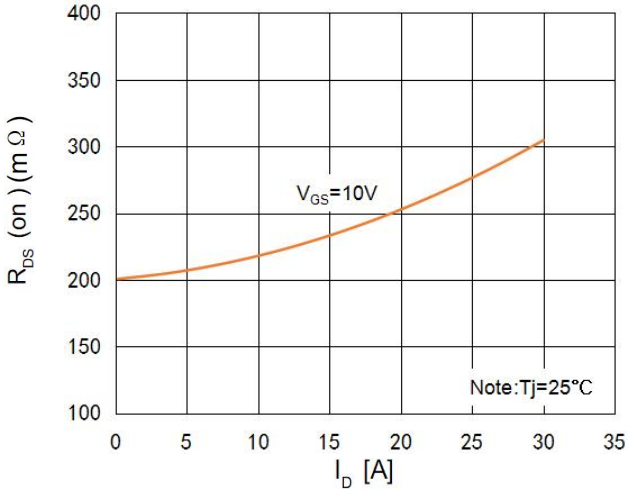
On-Regin Characteristics



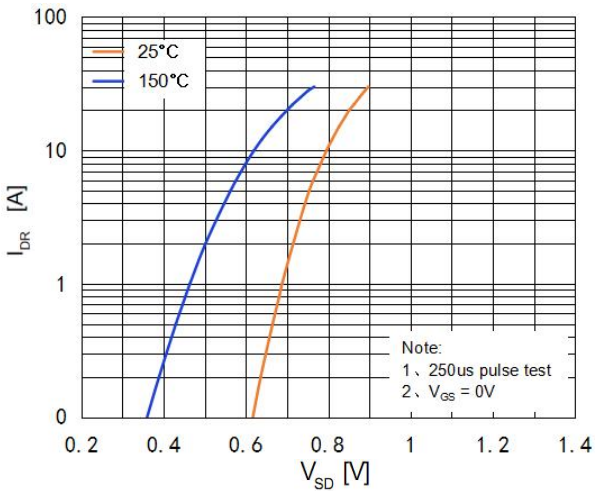
Transfer Characteristics



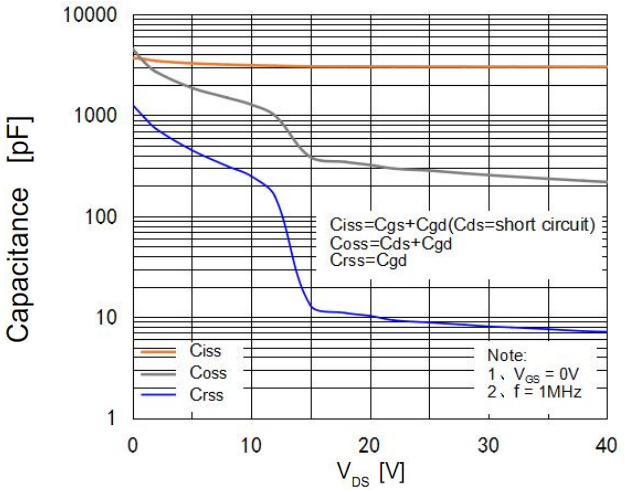
On-Resistance Variation vs. Drain Current and Gate Voltage



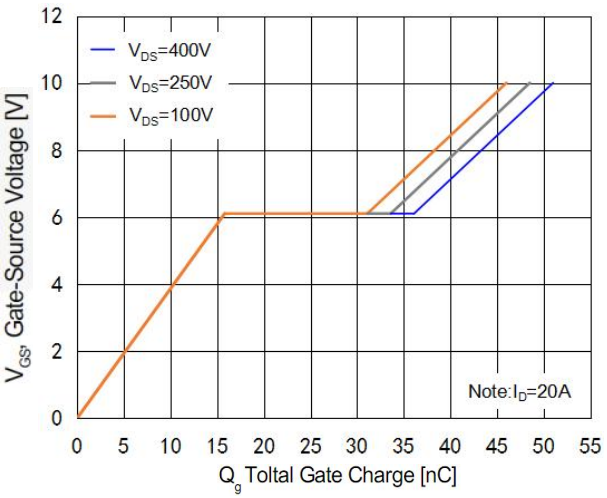
Body Diode Forward Voltage Variation vs. Source Current and Temperature



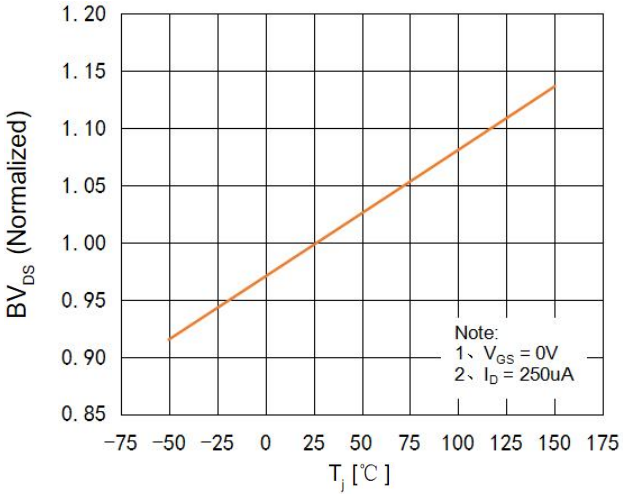
Capacitance Characteristics



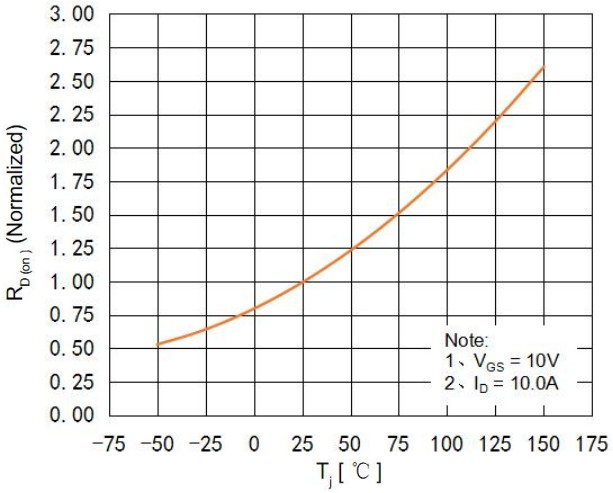
Gate Charge Characteristics



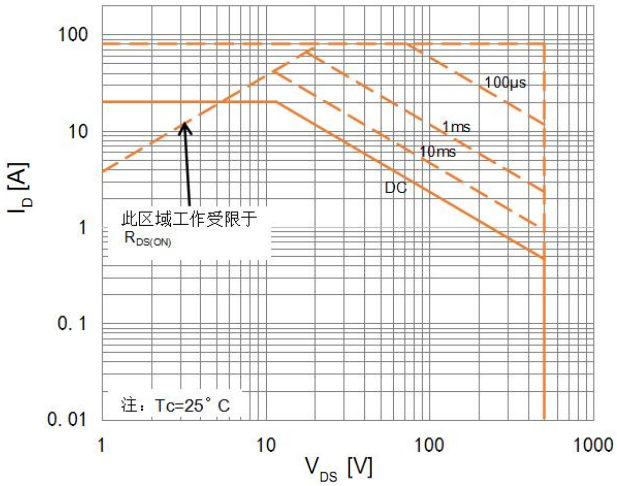
Breakdown Voltage Variation vs. Temperature



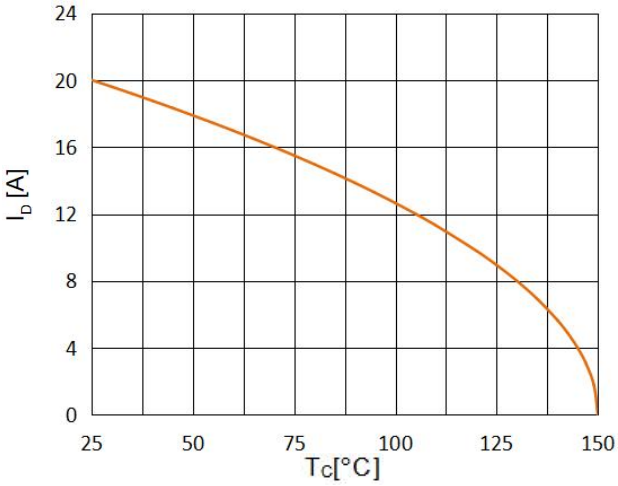
On-Resistance Variation vs. Temperature



Maximum Safe Operating Area



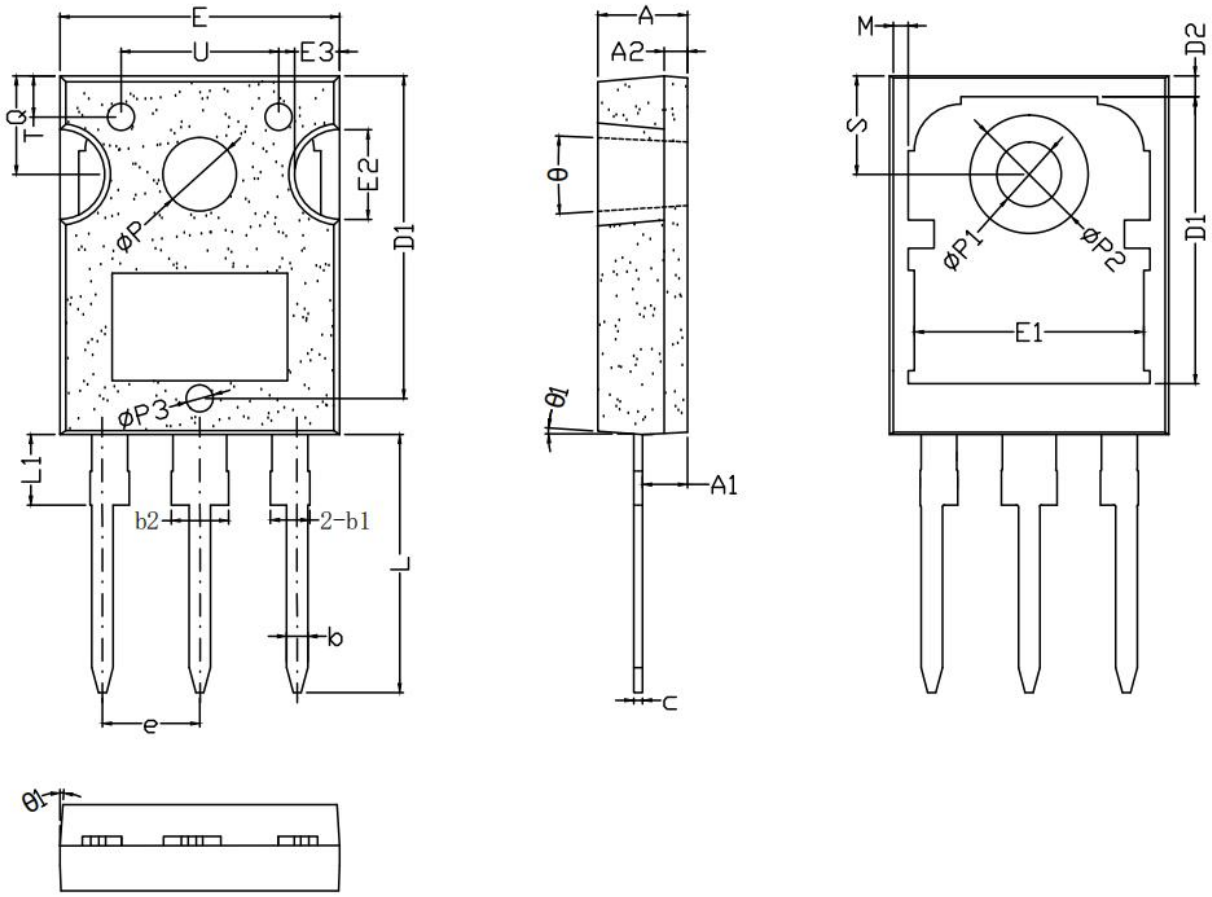
Maximum Drain Current Vs. Case Temperature



TO-247S Package Dimensions

UNIT：mm

SYMBOL	min	nom	max	SYMBOL	min	nom	max
A	4.80	5.00	5.20	e		5.44REF	
A1	2.30	2.50	2.70	L	12.90	14.40	15.90
A2	1.10	1.30	1.50	L1			4.30
b	1.10	1.20	1.30	ΦP	3.90	4.10	4.30
b1	1.90	2.10	2.30	ΦP1	3.40	3.60	3.80
b2	2.85	3.10	3.35	ΦP2	6.30	6.60	6.90
C	0.40	0.50	0.60	ΦP3	1.35	1.50	1.65
D	18.50	20.0	21.50	Q	5.00	5.50	6.00
D1	14.50	16.00	17.50	S	5.20	5.50	5.80
D2	1.00	1.15	1.30	T	2.10	2.30	2.50
E	14.10	15.60	17.10	U	8.30	8.80	9.30
E1	12.30	13.30	14.30	θ	3°	6°	9°
E2	4.80	5.00	5.20	θ 1	3°	6°	9°
E3	2.30	2.50	2.70				



注意事项：

1、在电路设计时请不要超过器件的最大额定值，否则会影响整机的可靠性。

2、MOSFET产品为静电敏感型器件，使用时应注意采取防静电保护措施，如佩戴防静电手环、设备接地等。

3、如需安装散热片，请注意控制扭力大小及散热片的平整度。

4、该规格书由华科公司制作，并可能不定期更改，恕不另行通知。

5、如有疑问，请及时联系我司销售代表。

版本履历表：

序号	版本号	修改时间	修改记录
1	V1.0	2022-12-20	首次发行